SPECIFICATION AMENDMENTS

Please amend the paragraph added before the first paragraph of the specification in the Preliminary Amendment filed on November 19, 2003, as follows:

This application is a continuation of prior Application Serial No. 09/363,547, filed on July 29, 1999, now U.S. Patent No. 6,654,833, issued on November 25, 2003.

Please amend the paragraph beginning on line 6 of page 2 of the specification as follows:

In one embodiment of the invention, a method for use with a computer system includes permitting a first bus agent to access a bus during predetermined windows of time and preventing a ~~second~~ first bus agent from accessing the bus outside of the windows. The first bus agent has a higher priority than the second bus agent. Use of the bus by the second bus agent is monitored during the windows, and the duration of the windows are selectively regulated based on the use.

Please amend the paragraph beginning on line 12 of page 2 of the specification as follows:

In another embodiment, a method for use with a computer system includes permitting a first bus agent to access a bus during predetermined windows of time ~~and preventing a second bus agent from accessing the bus outside of the windows~~. The first bus agent has a lower priority than the second bus agent. The second bus agent is prevented from accessing the bus during the windows, and use of the bus by the first bus agent is monitored during the windows. The durations of the windows are selectively regulated based on the use.

Please amend the paragraph beginning on line of page 8 of the specification as follows:

The PCI bus 38 may also be coupled to a modem 46 and a south bridge 36 that provides an interface to an input/output (I/O) expansion bus 40, a CD-ROM drive 50 and the hard disk drive 48. An I/O controller 54 may be coupled to the I/O expansion bus 40 and receive input data from a mouse 56 and a keyboard 58. The I/O controller 54 may also control operations of a floppy disk drive 52. As depicted in Fig. 2, the computer system 30 includes a display controller 43 that is connected to the AGP bus 47. A display 45 is connected to the display controller 43.